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PATENT  
USSN 10/038,990



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Seung Ki Joo et al.  
Serial No.: 10/038,990  
Filing Date: January 4, 2002  
Examiner: Thao X. Le  
Group Art Unit: 2814  
Title: *Method of Fabricating a Semiconductor Device  
Including a Crystalline Active Silicon Layer*

**MAIL STOP AF**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

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*Willie Jiles*

Willie Jiles

**RESPONSE PURSUANT TO 37 C.F.R. § 1.116**

Applicants hereby respond to the Office Action dated April 23, 2003.